À COMISSÃO DE LICITAÇÃO

Processo Administrativo virtual nº 2011/4757
ASSUNTO: Contratação de Empresa especializada para o fornecimento de 68(sessenta e oito) servidores de dados para atender ao Justiça do Estado do Amazonas.

INFORMAÇÃO

Conforme solicitado por esta CPL, segue ANÁLISE TÉCNICA da proposta classificada em terceiro lugar, **item 2**, constante nas páginas 1067 a 1078 do processo em epígrafe (TJ/AM 2011/4757), onde foi constatado que a proposta **atende integralmente** a configuração do equipamento exigida no Termo de Referência.
A proposta foi auditada no site do fabricante e em consulta aos órgãos emissores de índices e comprovações de atendimento (SPEC, VMWARE), conforme solicitados no Termo de Referência.

Referências:
1- Guia de Especificações Técnicas do produto ofertado:
http://www.dell.com/downloads/global/products/pedge/en/server-poweredge-t610-tech-guidebook.pdf

2- Índice SPEC do processador ofertado, consultado direto no site do órgão:
http://www.spec.org/cpu2006/results/res2010q2/cpu2006-20100412-10443.pdf

3- Compatibilidade com VMWARE:
http://www.vmware.com/resources/compatibility/detail.php?deviceCategory=server&productid=13803&deviceCategory=server&partner=23&systemTypes=3&page=3&display_interval=10&sortColumn=Partner&sortOrder=Asc

4- Índice SPEC encaminhado pelo fabricante: em anexo.

Manaus, 27 de março de 2012

Adriano Luiz do Vale Soares
Analista Judiciário, Especialista em TI

SPEC® CINT2006 Result

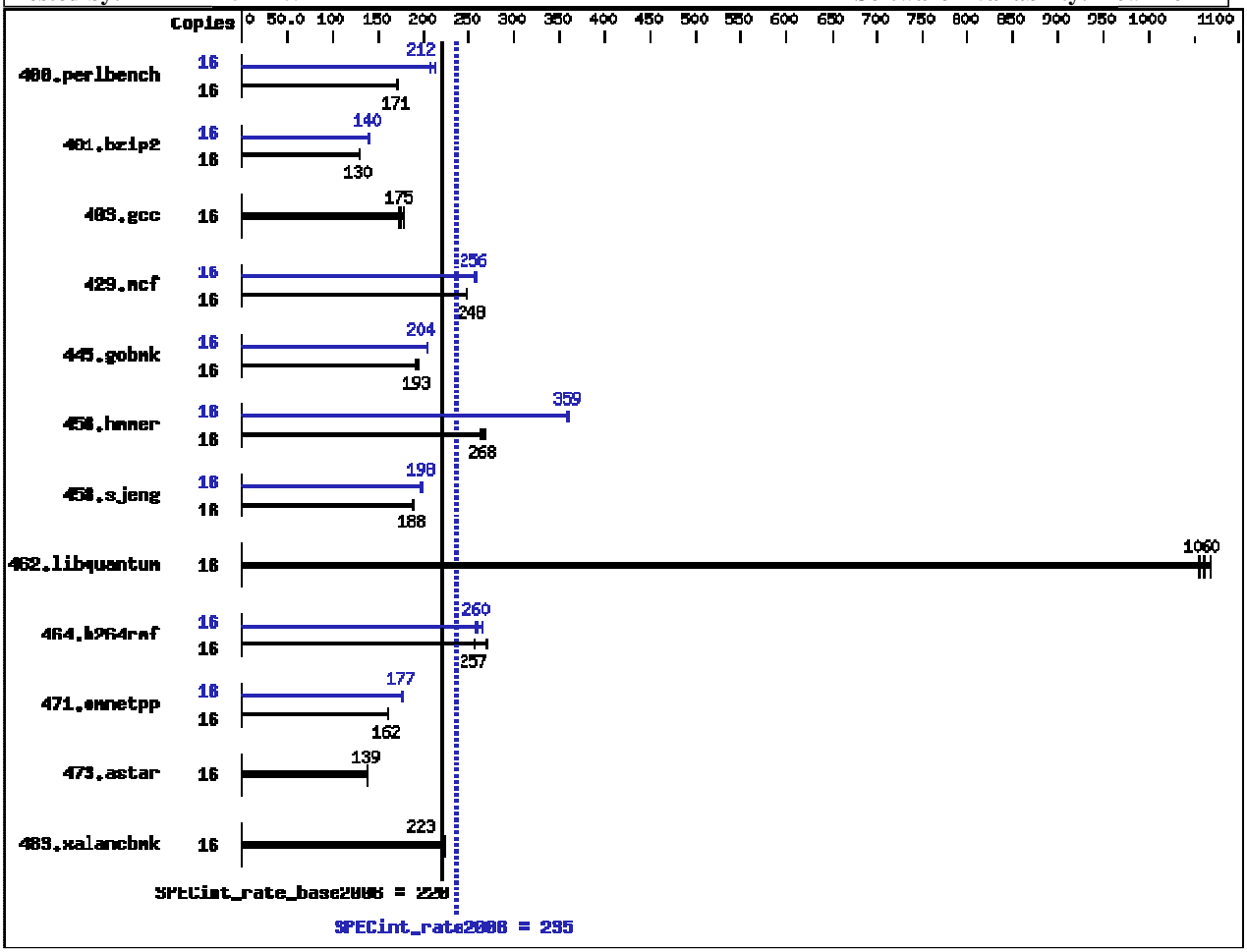
Dell Inc.
PowerEdge T610 (Intel Xeon E5620, 2.40 GHz)

SPECint®_rate2006 = 235

SPECint_rate_base2006 = 220

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2011
Hardware Availability: Mar-2010
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon E5620
CPU Characteristics:	Intel Turbo Boost Technology up to 2.66 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem:	2 x 146 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	909	172	**914**	**171**	915	171	16	**739**	**212**	737	212	752	208
401.bzip2	16	1190	130	**1186**	**130**	1186	130	16	1103	140	1099	140	**1100**	**140**
403.gcc	16	726	177	**737**	**175**	743	173	16	726	177	**737**	**175**	743	173
429.mcf	16	589	248	**589**	**248**	588	248	16	563	259	570	256	**570**	**256**
445.gobmk	16	864	194	871	193	**871**	**193**	16	823	204	**824**	**204**	826	203
456.hmmer	16	567	263	**557**	**268**	555	269	16	417	358	415	359	**416**	**359**
458.sjeng	16	**1030**	**188**	1031	188	1028	188	16	977	198	**978**	**198**	979	198
462.libquantum	16	**313**	**1060**	310	1070	314	1050	16	**313**	**1060**	310	1070	314	1050
464.h264ref	16	1309	270	1378	257	**1376**	**257**	16	**1363**	**260**	1332	266	1365	259
471.omnetpp	16	**619**	**162**	618	162	619	162	16	565	177	566	177	**566**	**177**
473.astar	16	**811**	**139**	810	139	812	138	16	**811**	**139**	810	139	812	138
483.xalancbmk	16	495	223	496	223	**495**	**223**	16	495	223	496	223	**495**	**223**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Data Reuse = Disabled (Default = Enabled)
```

General Notes

```
The Dell PowerEdge T610 and
the Bull NovaScale T840 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge T610 model
Binaries were compiled on RHEL5.5
```

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/smartheap -lsmartheap -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html,
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html.

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml,
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Mar 20 23:20:13 2012 by SPEC CPU2006 HTML formatter v6524.